



JPW

Docket No.: 204552016501  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Yuichi SATO

Application No.: 10/774,536

Confirmation No.: 3216

Filed: February 10, 2004

Art Unit: 2814

For: STATIC RANDOM ACCESS MEMORY AND  
SEMICONDUCTOR DEVICE USING MOS  
TRANSISTORS HAVING CHANNEL REGION  
ELECTRICALLY CONNECTED WITH GATE

Examiner: Douglas A. Wille

**RESPONSE UNDER 37 CFR 1.111**

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

**INTRODUCTORY COMMENTS**

This is in response to the non-final Office Action dated May 13, 2005. Reconsideration and allowance of the pending claims in light of the remarks presented herein are respectfully requested.

**Remarks/Arguments** begin on page 2 of this paper.